501.37465CC9

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Y. TANABE, et al.

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Serial No.:

10/774.589

APR 2 0 2005

Filed:

February 10, 2004

Title:

METHOD FOR FABRICATING SEMICONDUCTOR

INTEGRATED CURCUIT DEVICE

Group:

2818

Examiner:

David Vu

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Confirmation No.:

1087

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## INFORMATION DISCLOSURE STATEMENT **UNDER 37 CFR 1.97 AND 1.98**

Mail Stop: ISSUE FEE Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

April 20, 2005

SIR:

Pursuant to Applicants' duty of disclosure, enclosed please find a copy of a Supplementary Partial European Search Report in connection with an European Patent Application corresponding to the above-identified application. Also enclosed are copies of the documents cited in this Supplementary Partial European Search Report. Further enclosed is a Form PTO/SB/08A and B, listing the enclosed documents.

This Information Disclosure Statement is being submitted subsequent to issuance of Notice of Allowance in connection with the above-identified application, but prior to payment of the Issue Fee. For satisfying the requirements of 37 CFR 1.97(d), the fee set forth in 37 CFR 1.17(p) is enclosed, and the following Statement under 37 CFR 1.97(e) is made:

> Each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

Docket No. 501.37465CC9 Serial No. 10/774,589 April 20, 2005

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In view of the foregoing, it is respectfully submitted that all applicable requirements of 37 C.F.R. 1.97 and 1.98 have been satisfied, in connection with the present submission of the enclosed documents. Accordingly, consideration of the enclosed documents; and subsequent thereto, issuance of a U. S. Patent based upon the above-identified application in due course, are respectfully requested.

Applicants request any shortage or excess in fees in connection with the filing of this paper, including extension of time fees, and for which no other form of payment is offered, be charged or credited to Deposit Account No. 01-2135 (Case: 501.37465CC9).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP.

William I. Solomon / Registration No. 28,565

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April 20, 2005

APR 2 0 2005

PTO/SB/08A (08-03) Approved for use through 07/31/2008. OMB 0851-0031 midemark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO INFORMATION DISCLOSURE				Complete if Known			
				Application Number	10/774,589		
	ATEMENT BY			Filing Date	02/10/2004		
	(use as many sheets	es necessar	Y)	First Named Inventor	Y. TANABE		
				Art Unit	2818		
<u> April 20, 2005</u>				Examiner Name	David Vu		
Sheet	1	of	Z	Attorney Docket Number	501.37465CC9		

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		US-4,139,658	02	/1979		. Cohen, et al.			_
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Petent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS, SEND TO: Commissioner for Patenta, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTC/SB/08B (04-03)
Approved for use through 10/31/2002. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Substitute for for				Complete If Known			
				Application Number	10/774,589		
INFO	RMATIO	N DISC	LOSURE	Filing Date	02/10/2004		
STA	TEMENT	BY API	PLICANT	First Named Inventor	Y. TANABE		
	April	<u> 20, 2005</u>		Group Art Unit	2818		
	(use as many s	heets as nec	ssary)	Examiner Name	David Vu		
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Examiner Initials*	Cite No.	item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s),	Τ²
		Y. Tanabe, et al., "Diluted Wet Oxidation: A Novel Technique for Ultra Thin Gate Oxide Formation", IEEE International Symposium on Semiconductor Manufacturing Conference Proceedings, pp. 49-52, October 1997. (XP000849844)	
		E. Kool, et al., "Formation of Silicon Nitride at a Si-SiO <sub>2</sub> Interface during Local Oxidation of Silicon and during Heat-Treatment of Oxidated Silicon in NH <sub>2</sub> Gas", <u>Journal of Electrochemical Society</u> , Vol. 123, No. 7, pp. 1117-1120, July 1976.	
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